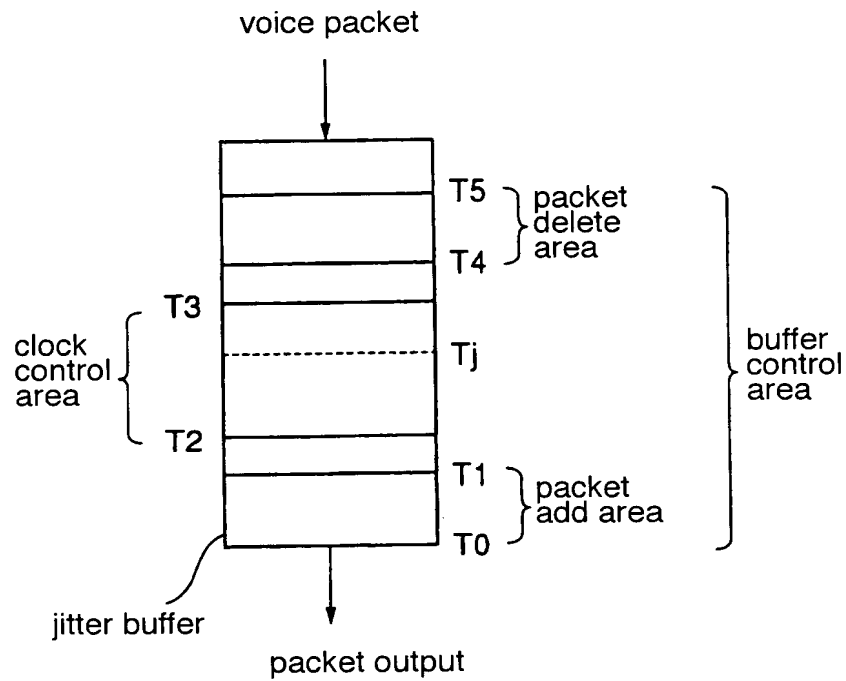


FIG. 1



Tj: current stored packet quantity
 T0: lower limit of packet add area
 T1: upper limit of packet add area
 T2: lower fluctuating value of clock control area
 T3: upper fluctuating value of clock control area
 T4: lower limit of packet delete area
 T5: upper limit of packet delete area

FIG. 2(A)

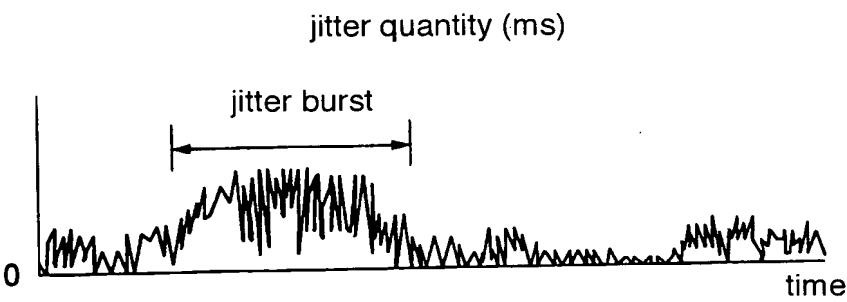


FIG. 2(B)

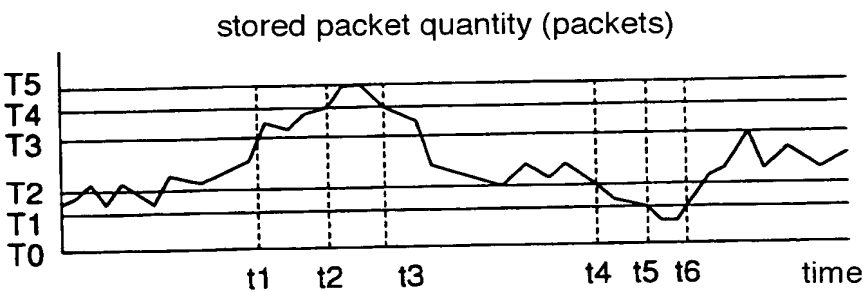


FIG. 2(C)

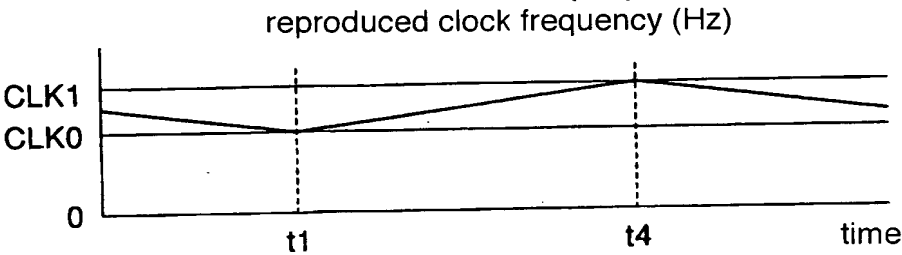


FIG. 2(D)

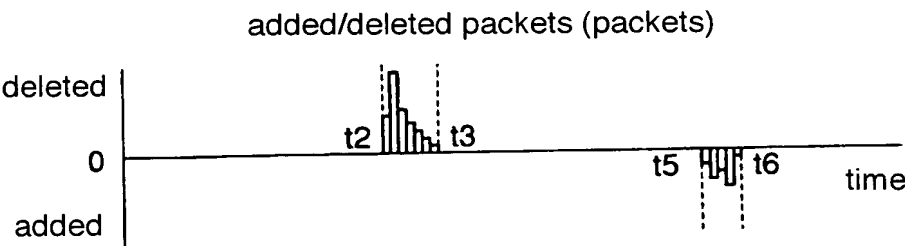


FIG. 3

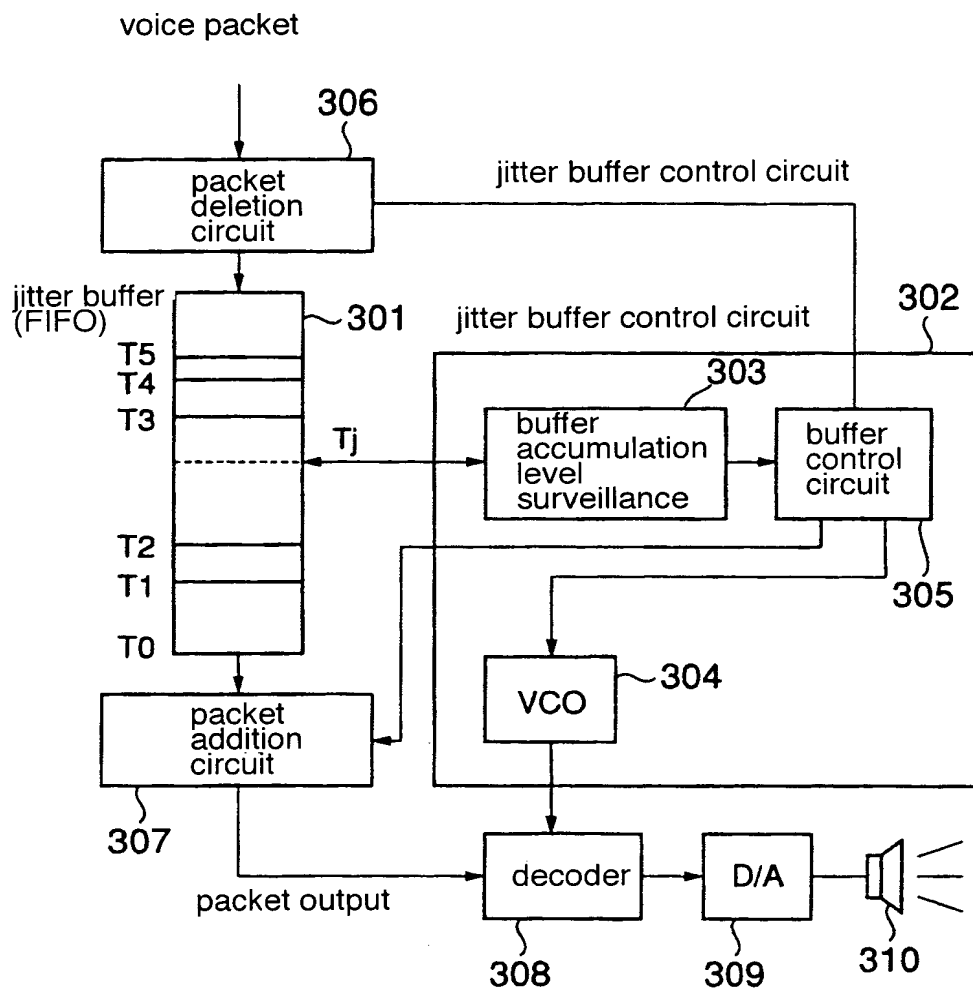
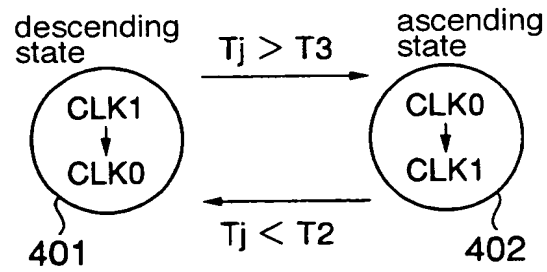


FIG. 4(A)

state transition of clock frequency

**FIG. 4(B)**

addition/deletion of voice packets

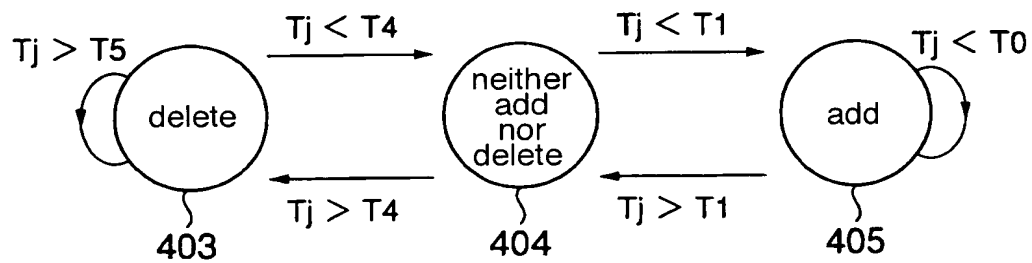


FIG. 5

